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10/528,332

03/18/2005

Valery Vasilievich Ovchinnikov

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LADAS & PARRY LLP
26 WEST 61ST STREET
NEW YORK, NY 10023

EXAMINER

TORRES, JUAN A

ART UNIT

PAPER NUMBER

2611

MAIL DATE

DELIVERY MODE

05/19/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

| | | | |
|------------------------------|--------------------------------------|---|--|
| Office Action Summary | Application No. 10/528,332 | Applicant(s) OVCHINNIKOV, VALERY VASILIEVICH | |
| | Examiner JUAN A. TORRES | Art Unit 2611 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 March 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 18 March 2005 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION***Priority***

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Drawings

The subject matter of this application admits of illustration by a drawing to facilitate understanding of the invention. Applicant is required to furnish a drawing under 37 CFR 1.81(c). No new matter may be introduced in the required drawing. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d).

Specification

Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

The abstract of the disclosure is objected to because:

a) exceeds 150 words in length

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b) uses form and legal phraseology often used in patent claims, such as "said," (see lines 4 and 6). Correction is required. See MPEP § 608.01(b).

c) the recitation "resister" in lines 11 and 12 is improper, because it is improperly constructed (see line 6); it is suggested to be changed to "resistor"

The disclosure is objected to because of the following informalities: the disclosure is objected to because it contains an embedded hyperlink and/or other form of browser-executable code (see page 3 line 2 "www.Dalsemi.com").

Applicant is required to delete the embedded hyperlink and/or other form of browser-executable code. See MPEP § 608.01.

Appropriate correction is required.

The use of the trademark "Dallas Semiconductor" (see page 2 line 2) has been noted in this application. It should be capitalized wherever it appears and be accompanied by the generic terminology.

Although the use of trademarks is permissible in patent applications, the proprietary nature of the marks should be respected and every effort made to prevent their use in any manner which might adversely affect their validity as trademarks.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 1 provides for the use of "A method for transferring of discrete electrical signals from a transmitter to a receiver", but, since the claim does not set forth any steps involved in the method/process, it is unclear what

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method/process applicant is intending to encompass. A claim is indefinite where it merely recites a use without any active, positive steps delimiting how this use is actually practiced.

Claim 1 is rejected under 35 U.S.C. 101 because the claimed recitation of a use, without setting forth any steps involved in the process, results in an improper definition of a process, i.e., results in a claim which is not a proper process claim under 35 U.S.C. 101. See for example *Ex parte Dunki*, 153 USPQ 678 (Bd.App. 1967) and *Clinical Products, Ltd. v. Brenner*, 255 F. Supp. 131, 149 USPQ 475 (D.D.C. 1966).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant Admitted Prior Art (AAPA) in view of Lattice ("In-System Programming Design Guidelines for ispJTAG Devices", February 2002).

Regarding claim 1, AAPA discloses a method for transferring of discrete electrical signals from a transmitter to a receiver, which are located in a two-wire communication line with a power voltage supply where the first pole of the power supply and the first wire of the communication line are grounded while the second wire of the communication line is connected to the second pole of the

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power supply via a resistor, comprising binary-code transfer of a logic signal through closing of the line by the receiver using an electric key and reading, by the receiver, of the value of voltage in the second wire and both signal transfer and the signal's voltage value reading are referred to the first wire of the communication line (page 3 lines 1-12). AAPA doesn't disclose that the first wire of the communication line is grounded via an additional resistor whose value is equal to the value of the first resistor. Lattice discloses that the first wire of the communication line is grounded via an additional resistor whose value is equal to the value of the first resistor (page 6 "pin connections after programming" section). AAPA and Lattice teachings are analogous art because they are from the same field of communication systems. At the time of the invention it would have been obvious to a person of ordinary skill in the art to incorporate in the methodology disclosed by AAPA the ground resistor disclosed by Lattice. The suggestion/motivation for doing so would have been to improve the noise immunity (page 6 "pin connections after programming" section).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

- a) Maxim ("DS2409 MicroLAN Coupler"), February 7, 2003.
- b) Pelkey (US 3732493 A) discloses an Electrical Compensating Circuit using a pull down resistor to increase the noise immunity.
- c) Polyak (US 5014315 A) discloses a digital telephone system, having a through-switching module connected by its inputs/outputs to "1" subscriber

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concentration modules, each with group subscriber inputs/outputs connected via remote subscriber modules to groups of telephone sets. The telephone sets are digital telephone sets with inputs/outputs connected to individual subscriber inputs/outputs of remote subscriber modules via digital individual subscriber lines to provide transmission and reception of digitally coded information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JUAN A. TORRES whose telephone number is (571)272-3119. The examiner can normally be reached on 8-6 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mohammad Ghayour can be reached on 571-272-3021. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Juan Alberto Torres
5/16/2008

/Juan A Torres/
Examiner, Art Unit 2611